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NTE5679 TRIAC – 600V, 40A

Description

The NTE5679 TRIAC may be gate triggered from a blocking to conduction state for either polarity of applied voltage and is designed for AC switching and phase control applications such as speed and temperature modulation controls, lighting controls, and static switching relays. The triggering signal is normally applied between the gate and MT₁.

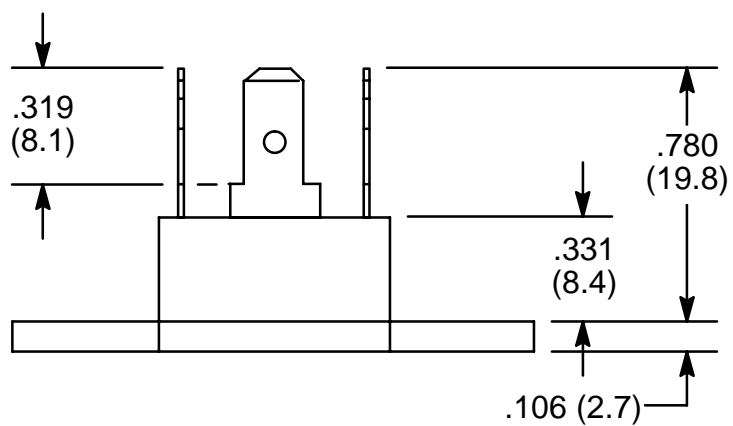
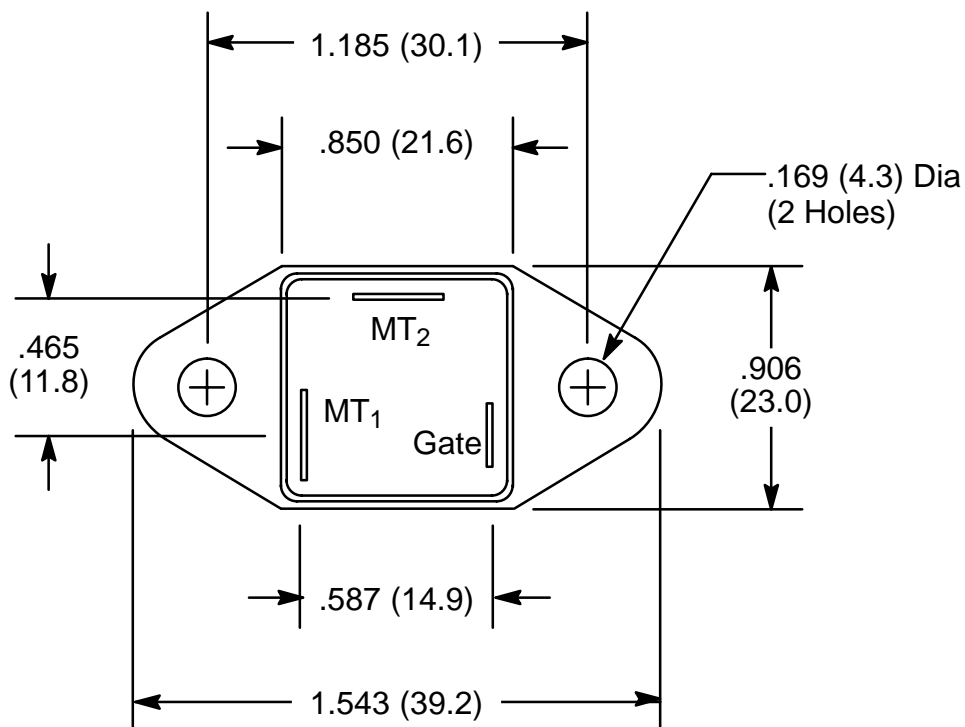
Electrical Characteristics: (All measurements are at T_A = +25°C, 60Hz with a resistive load unless otherwise specified)

Repetitive Peak Blocking Voltage (Note 1), V _{DRM}	600V
RMS On-State Current (Conduction Angle of 360°, T _C = 0° to +125°C), I _{T(RMS)}	40A
Peak Off-State Current (V _{DRM} = 600V, Gate Open, Note 1), I _{DRM}	
T _C = +25°C	200mA
T _C = +125°C	5mA
Peak One-Cycle Surge Current, I _{TSM}	
50Hz	335A
60Hz	400A
Non-Repetitive RMS Surge On-State Current for Fusing (I _{GT} = 500mA, 8.3ms), I ² t	664A ² sec
Maximum DC Gate Trigger Current (V _D = 12V, Quad I, II, III), I _{GT}	100mA
Maximum DC gate Trigger Voltage (V _D = 12V, Quad I, II, III. Note 2), V _{GT}	
T _C = +25°C	2.5V
T _C = +125°C	0.2V
Peak On-State Voltage (I _{T(RMS)} = 40A, T _C = +25°C, Note 1), V _{TM}	1.8V
DC Holding Current (gate Open, Note 1, Note 3), I _H	120mA
Peak Gate Trigger Current (Pulse Width ≤ 10μs), I _{GTM}	4A
Peak Gate Power Dissipation (Pulse Width ≤ 10μs), P _{GM}	40W
Average Gate Power Dissipation, P _{G(AV)}	800mW
Minimum Critical Rate of Rise of Off-State Voltage (V _{DRM} = 600V, Gate Open, Note 1), dv/dt	
T _C = +100°C	375V/μs
T _C = +125°C	250V/μs
Minimum Critical Rate of Rise of Commutation Voltage (Note 1), dv/dt _(c)	
(V _{DRM} = 600V, I _{T(RMS)} = 40A, Commutating di/dt = 21.6A/msec, Gate Unenergized)	4V/μs
Maximum Rate of Change of On-State Current (I _{GT} = 200mA, Rise Time = 0.1μs), di/dt .	150A/μs
Maximum Gate Controlled Turn-On Time (I _{GT} = 500mA, Rise Time = 0.1μs), t _{gt}	5μs
Operating Temperature Range, T _J	0° to +125°C
Storage Temperature Range, T _{stg}	-20° to +125°C

Note 1. For either polarity of MT₂ with reference to MT₁ terminal.

Note 2. For either polarity of gate voltage (V_{GT}) with reference to MT₁ terminal.

Note 3. Initial On-State Current = 400mA (DC).



All Dimensions are Max