

NTE2030 Integrated Circuit High Voltage Segment Driver for Gas Discharge Tubes

Description:

The NTE2030 is capable of driving 8 segments of a high voltage display tube with a constant output sink current, which can be adjusted by external program resistor, R_p . The program current is half that of output "ON" current. In the "OFF" state the outputs can tolerate more than 80V. The ratio of "ON" output currents is within $\pm 10\%$. Inputs have negative clamp diodes. Active high input logic. The main application of the device is to interface MOS circuits to high-voltage displays.

Features:

- Versatile Circuits for a Wide Range of Display Applications
- High Breakdown Voltages
- Low Power Dissipation

Absolute Maximum Ratings:

Input Voltage (Note 2) -20V
Output Voltage 85V
Operating Temperature Range 0° to +70°C
Storage Temperature Range -65° to +150°C
Lead Temperature (During Soldering, 10sec) +300°C

Note 1 "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2 All voltage for NTE2030 with respect to $V_{CC} = 0V$.

DC Electrical Characteristics: (Note 2 & Note 3)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Current	I_I	$V_{IN} = 6.0V$	150	250	350	μA
Logical "0" Input Current	I_{IL}	$I_{OUT} = 5.0\mu A$, $V_{OUT} = 75V$	-	-	7.0	μA
Logical "1" Input Current	I_{IH}	$I_{OUT} = 1.4mA$, $I_{IP} = 850\mu A$, $V_{OUT} = 50V$	80	-	-	μA
Input Clamp Voltage	V_I	$I_{IN} = -1.0mA$, $T_A = +25^\circ C$	-	-0.68	-0.85	V

Note 2 All voltage for NTE2030 with respect to $V_{CC} = 0V$.

Note 3 Unless otherwise specified, Min/Max limits apply across the 0°C to +70°C. All typicals are given for $T_A = +25^\circ C$.

DC Electrical Characteristics (Cont'd): (Note 2 & Note 3)

Parameter	Symbol	Test Conditions		Min	Typ	Max	Unit
Output Breakdown Voltage	V _{OH}	I _{OUT} = 100μA, I _{IN} = 0μA		80	-	-	V
Output Leakage Current	I _{CEX}	V _{OUT} = -75V, -0.1mA ≤ I _{IN} ≤ 7.0μA		-	0.02	5.0	μA
Prog Input Voltage	I _{PROG}	I _{IP} = 150μA		1.8	2.3	-	V
				-	4.0	4.5	
Logical “0” Output Current	I _{OL}	V _{OUT} = 50V, 80μA ≤ I _{IN} ≤ I _{IP}	I _{IP} = 150μA	240	300	360	μA
			I _{IP} = 400μA	680	800	920	μA
			I _{IP} = 850μA	1.53	1.7	1.87	mA
Output Current Ratio	ΔI _O	I _{OUT} b Ref = 1.7mA, V _{OUT} = 50V		0.9	1.0	1.1	

Note 2 All voltage for NTE2030 with respect to $V_{CC} = 0V$.

Note 3 Unless otherwise specified, Min/Max limits apply across the $0^\circ C$ to $+70^\circ C$. All typicals are given for $T_A = +25^\circ C$.

Note 4 Supply currents specified for any one input = -1.0V. All other inputs = -5.5V and selected output having 16mA load.

AC Electrical Characteristics: ($T_A = +25^\circ C$, unless otherwise indicated)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Propagation Delay to a Logical "0" from Input to Output	t_{pd0}	$R_P = 6k$ to $6V$, $R_{OUT} = 1k$ to $6V$	-	37	100	ns
Propagation Delay to a Logical "1" from Input to Output	t_{pd1}	Input Ramp Rate $\leq 15ns$, Freq = 1MHz, DC = 50%, Amplitude = 6.0V	-	92	200	ns

Note 2 All voltage for NTE2030 with respect to $V_{CC} = 0V$.

Note 3 Unless otherwise specified, Min/Max limits apply across the $0^\circ C$ to $70^\circ C$. All typicals are given for $T_A = +25^\circ C$.

Note 4 Supply currents specified for any one input = -1.0V. All other inputs = -5.5V and selected output having 16mA load.

Pin Connection Diagram

